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	Filing Date		2006-08-21	
	First Named Inventor	Nobuyuki YAMASAKI		
	Art Unit	2186		
	Examiner Name	Unassigned		
	Attorney Docket Number	4752-006		

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	1	EP 1233340	EP		2002-08-21	SIROYAN LIMITED		<input type="checkbox"/>
	2	EP 0942365	EP		1999-09-15	LUCENT TECHNOLOGIES INC.		<input type="checkbox"/>
	3	JP2000-242505	JP		2000-09-08	MITSUBISHI ELECTRIC CORP		<input type="checkbox"/>

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4	JP2000-076079	JP		2000-03-14	NEC CORP		<input type="checkbox"/>
5	JP10-011301	JP		1998-01-16	IMAI MASA HARU YAZAKI CORP		<input type="checkbox"/>
6	JP09-212371	JP		1997-08-15	NEC CORP		<input type="checkbox"/>
7	JP07-141208	JP		1995-06-02	SONY CORP		<input type="checkbox"/>
8	JP06-083639	JP		1994-03-25	MITSUBISHI ELECTRIC CORP		<input type="checkbox"/>
9	JP03-009431	JP		1991-01-17	NEC CORP		<input type="checkbox"/>
10	JP10-063517	JP		1998-03-06	NIPPON TELEGR & TELEPH CORP		<input type="checkbox"/>
11	JP2002-533807	JP		2002-10-08	KONINKLIJKE PHILIPS ELECTRONICS N.V.		<input type="checkbox"/>
12	JP2002-513182	JP		2002-08-05	SIEMENS INFORMATION COMMUNICATION		<input type="checkbox"/>

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1	Guangzuo, C. et. al, Parallel Replacement Mechanism for MultiThread, Advances in Parallel and Distributed Computing 1997 Proceedings, 1997, pages 338 to 334.	<input type="checkbox"/>
2	Wills, D.S. et al., Pica: An Ultra-Light Processor for High-Troughput Application, Computer Design: VLSI in Computers and Processors, 1993. ICCD '93. Proceedings, 1993, pages 410 to 114.	<input type="checkbox"/>
3	Kwak, H. et al., Effects of multithreading on cache performance, computers, IEEE Transactions on, 1999, pages 176 to 184.	<input type="checkbox"/>

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